



Comparison of APx DSIO to 2700/PSIA digital serial interface features and capabilities.

The APx DSIO and the PSIA for the 2700 Series offer very similar feature sets. With recent software updates, APx DSIO meets or exceeds the features and specifications of the PSIA. Here is a comparison of key features, vetted by AP Tech Support and Engineering.

This information is current as of November 20 2014.

FEATURE	DSIO	PSIA
Audio Data Channel count	1, 2, 4, 8 or 16 active in TDM single line 1, 2, 4 or 8 active in multi-line	256 time slots, 2 active
Word Width, bits	8 to 128 bits	8 to 32 bits
Bit Depth (audio "active" data length), bits	8 bits to 32 bits	8 to 24 bits
Independent master clocks	Yes - allows for testing of sample rate converters	No
Transmitter Connections		
	Master Clock OUT	Yes
	Master Clock IN	Yes
	Master Clock Scope Monitor	Yes
	N*Fs Clock OUT	n/a
	N*Fs Clock Scope Monitor	Yes
	Bit Clock OUT	Yes
	Bit Clock IN	Yes
	Bit Clock Scope Monitor	Yes
	Channel Clock OUT	Yes
	Channel Clock Scope Monitor	Yes
	Frame Clock OUT	Yes
	Frame Clock IN	Yes
	Frame Clock Scope Monitor	Yes
	Data OUT	Yes
	Data Scope Monitor	Yes
Receiver Connections		
	Master Clock OUT	Yes
	Master Clock IN	Yes
	Master Clock Scope Monitor	Yes
	N*Fs Clock OUT	n/a
	N*Fs Clock Scope Monitor	Yes
	Bit Clock OUT	Yes
	Bit Clock IN	Yes



FEATURE		DSIO	PSIA
	Bit Clock Scope Monitor	Yes	Yes
	Channel Clock OUT	Yes	Yes
	Channel Clock Scope Monitor	Yes	Yes
	Frame Clock OUT	Yes	Yes
	Frame Clock IN	Yes	Yes
	Frame Clock Scope Monitor	Yes	Yes
	Data IN	Yes	Yes
	Data Scope Monitor	Yes	Yes
Voltage Levels			
	TTL Family 5 V	No	Yes
	TTL Family 3.3 V	No	Yes
	CMOS Family 3.3 V	Yes	Yes
	CMOS Family 2.4/2.5V	Yes	Yes
	CMOS Family 1.8 V	Yes	Yes
Loopback		Internal, software switch	Requires cables
Audio Data			
	Single data line (TDM)	Yes	Yes
	Multiple data lines	1, 2 or 4	No
Audio Data Format			
	I ² S	Yes, one click	Yes, one click
	DSP	Yes, one click	Yes, manually configurable
	Custom	Yes	Yes
Audio Data Justification		Left or right	Left or right
Frame Pulse duration			
	One Bit Clock	Yes	Yes
	One Subframe	Yes	No
	50% duty cycle	Yes	Yes
Frame Clock Inversion		Yes	Yes
Frame Clock Shift One Bit Left		Yes	Yes
Selectable Bit Clock Edge Sync		Yes	Yes
Master Clock OUT rates		4 kHz to 49.152 MHz	2.048 MHz to 27.648 MHz
Master Clock IN rates		4 kHz to 49.152 MHz	8 kHz to 60 MHz
Frame Clock (Sample Rate)		4 kS/s to 216 kS/s	6.75 kS/s to 216 kS/s
Output Latency			
	Frame Clock (referenced to Bit Clock)	typ 3 ns	15 ns
	Data lines (referenced to Bit Clock)	typ 3 ns	15 ns



FEATURE		DSIO	PSIA
	Monitor ports (referenced to signal pin)	typ 10 ns	Unspecified
Input setup and hold			
	Frame setup (referenced to Bit Clock)	6 ns	7 ns
	Frame hold (referenced to Bit Clock)	2 ns	3 ns
	Data line setup (referenced to Bit Clock)	6 ns	7 ns
	Data line hold (referenced to Bit Clock)	2 ns	3 ns
Adjustable padding		No	Yes
Dynamic timing display		Yes	No
Scale audio frequency		Yes	Yes
Coded audio		Any form of coded audio ¹	A-law, μ -law ²
Clock Jitter		Yes ³	Yes

¹ DSIO will input or output any coded audio bitstream, including Dolby, DTS and telecommunications algorithms such as A-law and μ -law. However, the APx500 software will neither encode nor decode coded audio.

² PSIA will input or output A-law and μ -law coded bitstreams. Additionally, the AP2700 software will encode and decode A-law and μ -law.

³ Requires APx unit with AMC (Advanced Master Clock) option, such as APx555 or APx unit that has had the AMC option installed. Contact Service at service@ap.com to determine if this upgrade is possible for older APx units.