Model 588



1U Rack Mount 8 Channel DDG



Features

- Low Profile Design
- 8 Channels of Delay and Width
- 200pS Jitter, 250pS Resolution
- NI Certified Labview Drivers Included
- Dual Trigger Option

Key Specifications

- 50pS internal jitter,200pS trigger jitter
- 250pS Delay & Width resolution
- USB and Ethernet Interfaces
- Custom Modes (Burst, Divide-by-N, etc)
- Multiplex up to 8 channels
- Trigger Inputs (Single or Dual)

Applications

- Automated Test Environments
- OEM System Components
- Remote Experimentation
- Ballistics, Downrange Test
- Syncronize Multiple Devices/Events

With the Model 588, we have packaged the precision pulse and digital delay timing designs in a convenient package for embedded and OEM applications. The new Model 588 1U DDG gives users 8 channels of precision timing in a package designed for remote applications and large system gating/syncronizing.

Our precision clock and many channel features compliment the low-profile packaging to give system integrators a refined rack-mount alternative to multiple boxes and cables cobbled together around a test setup. When it comes to custom timing requirements, our applications team is available to address your most demanding needs.



Specifications

MODULE SPECIFICATIONS

Output Impedance	50ohm		- A
TTL/CMOS Mode			Control of the Control
Output Level	4.0v typ into 1 kohm		
Rise time	3ns typ	STANDARD FEATURES/FU	INCHORS
Slew rate	0.5 V/ns	Communications	USB
Jitter	50ps RMS	External Clock In	10 /
Adjustable Mode			Use
Output Level	2.0 to 20 VDC into 1 kohm	External Clock Out	101
	1.0 to 10 VDC into 50 ohms		Use
Output Resolution	10mV	Configuration Stora	age 12 (
Current	200mA typical, 400 mA max		Aut
	(short pulses)		con
Slew Rate	0.1V/ns	PROGRAMMABLE TIMING G	ENERATOR
Overshoot	<100mV + 10% of pulse amplitude	Output Modes	Single Shot, E Continuous
TRIGGER/GATE DUAL INPUT MODULE (STANDARD) Standard dual channel input module, providing one trigger input and one gate input. May be used with the dual trigger		Control Modes	Internally Trig and External Each channel to any of the
firmware option to provid	e two independent trigger sources.	Output Multiplexer	Any/all chann
Threshold	0.2 to 15 VDC		any/all outpu
Maximum Input Voltage	60V Peak	Delayed Output	0 to 9,999,999
Resolution	10mV	Timebase	Same as Inter
Input Impedance	1Mohm + 40pF or 50ohm	Delays	
Insertion Delay	< 100 ns	Range	
Jitter	800ps RMS	Accurac	- у
		Resolut	ion
System External Trigger / Gate Input(s)		Pulse In	hibit Delay
Trigger input		Output	Inhibit Delay
	Generate individual nulses start		

Function	Generate individual pulses, start a burst or continuous stream.	
Rate	DC to 1/(200ns + longest active pulse)	
Slope	Rising or Falling	
Behavior	Used to control the Internal Rate Generator	
Gate Input		
Function	Pulse Inhibit or Output Inhibit	
Polarity	Active High / Active Low	
Behavior	Used to control the Internal Rate Generator or any of the	
	channels	

Communications	USB/RS232/Ethernet
External Clock In	10 MHz - 100 MHz
	User selectable in discrete values
External Clock Out	10 MHz - 100 MHz
	User selectable in discrete values
Configuration Storage	12 Configurations. Automatically saves current configuration on front panel

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Output Modes	Single Shot, Burst, Duty Cycle, Continuous	
Control Modes	Internally Triggered, Externally Triggered and External Gate Each channel may be independently set to any of the modes	
Output Multiplexer	Any/all channels any/all outputs	may be multiplexed to
Delayed Output	0 to 9,999,999 pt	ulses
Timebase	Same as Internal	Rate Generator
Delays		
Range		0 - 5000s
Accurac	СУ	1ns + .0001 x Delay
Resolut	ion	250ps

1	Incrementing (provides automatic high speed incrementing/decrementing of delay and/or pulsewidth for each channel)
DT15	Dual Trigger Logic – provides additional trigger via gate input

150ns

150ns

Other custom modules (led drivers, higher voltages, current sources) available. Call with your request.

System Specifications

Models/Outputs	- 4 independent output channels
	- 8 independent output channels
Inputs	2 inputs - 1 trig input / 1 gate input

INTERNAL RATE GENERATOR

Rate	0.0002 Hz to 20.000 MHz
Resolution	10ns
Accuracy	Same as time base
Jitter	50ps RMS
Settling	1 period
Burst Mode	1 to 9,999,999 pulses
Timebase	100 MHz, low jitter PLL
Oscillator	50 MHz, 25ppm
Output Modes	Single Pulse, Burst, Duty Cycle, Continuous
Control Modes	Internal Rate Generator, External Trigger,

External Gate

PROGRAMMABLE TIMING GENERATOR

Output Modes	Single Shot, Burs Continuous	st, Duty Cycle,
Control Modes	and External Gat	ay be independently set
Output Multiplexer	Any/all channels any/all outputs	may be multiplexed to
Delayed Output	0 to 9,999,999 p	ulses
Timebase	Same as Internal	Rate Generator
Delays		
Range		0 - 5000s
Accurac	y	1ns + .0001 x Delay
Resolut	ion	250ps
Pulse In	hibit Delay	150ns
Output	Inhibit Delay	150ns